PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: M. Loose

Examiner: C. Kao

Serial No. 09/671,409

Art Unit: 2882

Filing Date: September 27, 2000

For: IMAGER WITH ADJUSTABLE RESOLUTION

Assistant Commissioner for Patents Box Non-Fee Amendment Washington, D.C. 20231

AMENDMENT AFTER FINAL OFFICE ACTION

<u>TRANSMITTAL</u>

Sir:

Transmitted herewith is an amendment for this application. Applicant is a large entity.

Fee for Claims

	Claims Remaining After Amendment	Highest No. Previously Paid For	Present Extra	Rate	Addit. Fee
TOTAL	8	14	0	18.00	0.00
INDEP.	3	3	0	84.00	0.00

If any additional fee is required, charge Account No. 18-1750. A duplicate of this transmittal is attached.

Respectfully submitted,

August 30, 2002

Steven C. Patrick

Registration No.40,341 Attorney for Applicant

KOPPEL, JACOBS, PATRICK & HEYBL

555 St. Charles Drive, Suite 107 Thousand Oaks, California, 91360

Telephone: (805) 373-0060

M-S2-00SC053US3amend trans

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: M. Loose

Serial No. 09/671,409 Examiner: C. Kao

Filed: September 27, 2000 Art Unit: 2882

Title: IMAGER WITH ADJUSTABLE RESOLUTION

Assistant Commissioner for Patents

Box Non-Fee Amendment

Washington, D. C. 20231

AMENDMENT AFTER FINAL OFFICE ACTION

Sir:

In response to the Office Action dated July 19, 2002, kindly amend the above application as follows. Please note that, in accordance with 37 C.F.R. § 1.116, the present amendment only cancels claims and amends claims in compliance with requirements of form expressly set forth in the Examiner's previous Office action.

Claims

Please cancel claims 8 and 12.

Please amend claims 9, 13 and 15 as follows:

- 9. (amended) A photodetector array with selectable resolution, comprising:
 - a plurality of photodetectors;
- a switching circuit which configures neighboring ones of said photodetectors into pixels by directly summing at each pixel the outputs of multiple photodetectors into an aggregated pixel output, said aggregated pixel output stored on said pixel's intrinsic capacitance prior to being read out;

wherein said switching circuit is electronically